

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"5963104".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/05 09:24
S1	19	("20020169810" "20020188808" "20030050943 " "3961169" "4853884" "5422953" "6195433" "6792438" "6865678" "7096242").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/04 16:33
S2	2	"20050041803"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/04 15:44
S3	32	((efuse (fuse near memory)) and (random adj number) near3 (generat\$3 calculat\$4 manipulat\$4 computing produc\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/05 08:06
S4	124	((random adj number) near3 (generat\$3 calculat\$4 manipulat\$4 computing produc\$4)) same ((unmatch\$3 compar\$3 match\$3 judg\$3) with (random adj number) with (threshold))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/04 15:57
S5	6	("20020061031" "20020141376" "4987571" "6510322" "6560443" "6615050").PN. OR ("7099671").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/10/04 16:00
S6	10724	("ASIC" "IC" "FPGA" "PLC" "PLD" (integrated smart portable chip) adj (card circuit)) and ((random adj number) near3 (generat\$3 calculat\$4 manipulat\$4 computing produc\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/04 16:36
S7	317	380/44,47,46.ccls. and ("ASIC" "IC" "FPGA" "PLC" "PLD" (integrated smart portable chip) adj (card circuit)) and ((random near (number seed pseudo)) near3 (generat\$3 calculat\$4 manipulat\$4 computing produc\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/04 16:41
S9	167	708/250.ccls. and ("ASIC" "IC" "FPGA" "PLC" "PLD" (integrated smart portable chip) adj (card circuit)) and ((random near (number seed pseudo)) near3 (generat\$3 calculat\$4 manipulat\$4 computing produc\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/04 16:39
S10	1	S7 and ((efuse fuse) near memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/04 16:41

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S11	15	("ASIC" "IC" "FPGA" "PLC" "PLD" (integrated smart portable chip) adj (card circuit)) and ((random near (number seed pseudo)) near3 (generat\$3 calculat\$4 manipulat\$4 computing produc\$4)) and ((efuse fuse) near memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/04 16:51
S12	15	("ASIC" "IC" "FPGA" "PLC" "PLD" (integrated smart portable chip) adj (card circuit)) and ((random near (number seed pseudo)) near3 (generat\$3 calculat\$4 manipulat\$4 computing produc\$4)) and ((efuse fuse) near memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/05 08:22
S13	4	(efuse (fuse near (memory register))) and ((random adj number) near3 (stor\$4 input\$3) with (memory register))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/05 08:07
S14	4	((efuse fuse) near (memory register)) and ((random adj number) near3 (stor\$4 input\$3) with (memory register))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/05 08:17
S15	1	713/189.193.ccls. and ((efuse fuse) near (memory register)) and ((random adj number) near3 (stor\$4 input\$3) with (memory register))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/05 08:20
S18	12	711/100.ccls. and ("ASIC" "IC" "FPGA" "PLC" "PLD" (integrated smart portable chip) adj (card circuit)) and ((random near (number seed pseudo)) near3 (generat\$3 calculat\$4 manipulat\$4 computing produc\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/05 09:24
S19	17	716/16.ccls. and ("ASIC" "IC" "FPGA" "PLC" "PLD" (integrated smart portable chip) adj (card circuit)) and ((random near (number seed pseudo)) near3 (generat\$3 calculat\$4 manipulat\$4 computing produc\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/10/05 08:23